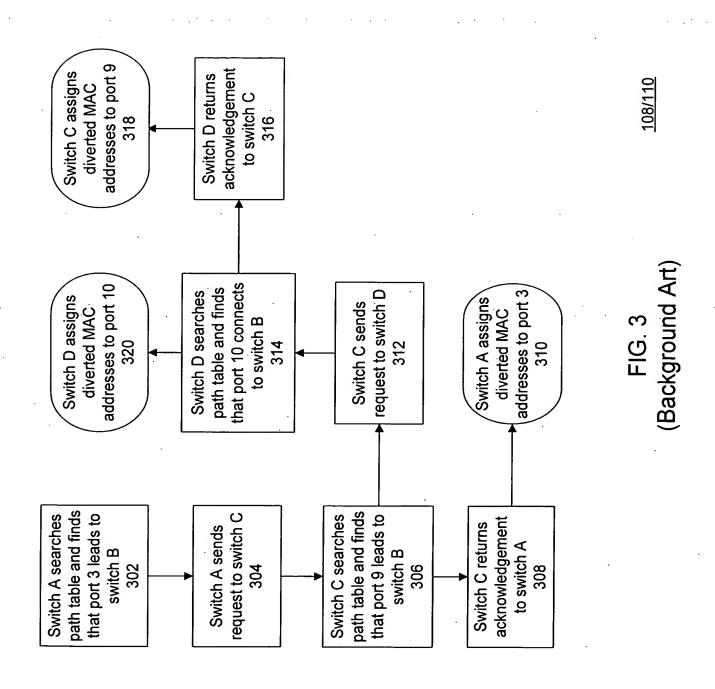


FIG. 2 (Background Art)



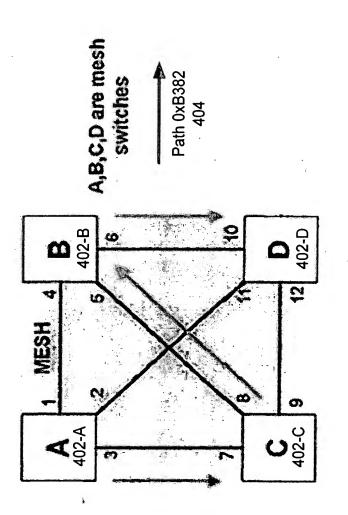
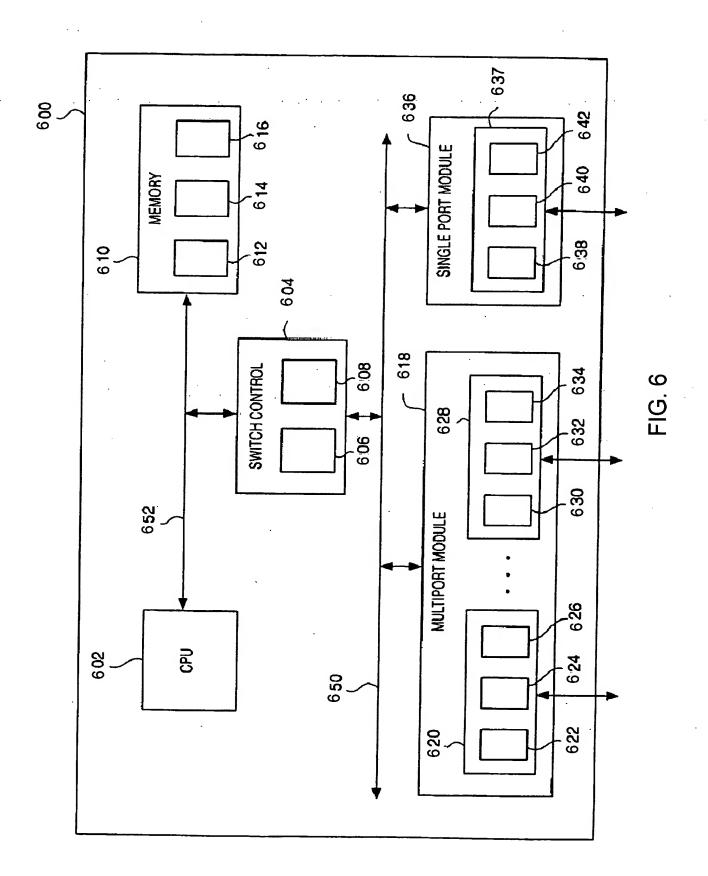
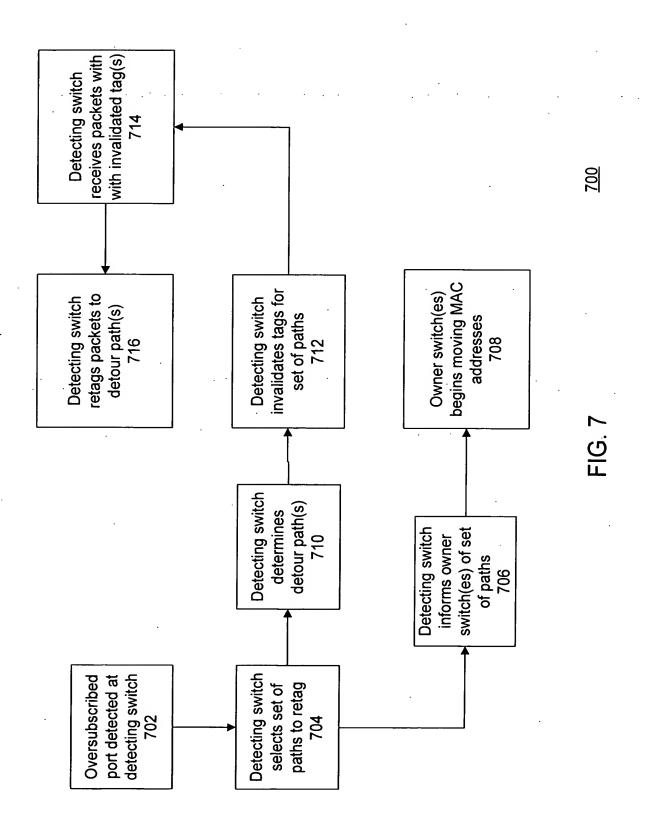


FIG. 1

	1	
Path Identifier (0-15) 506	4 bits	
Destination Switch Identifier 504	6 bits	FIG. 5
Source Switch Identifier 502	6 bits	





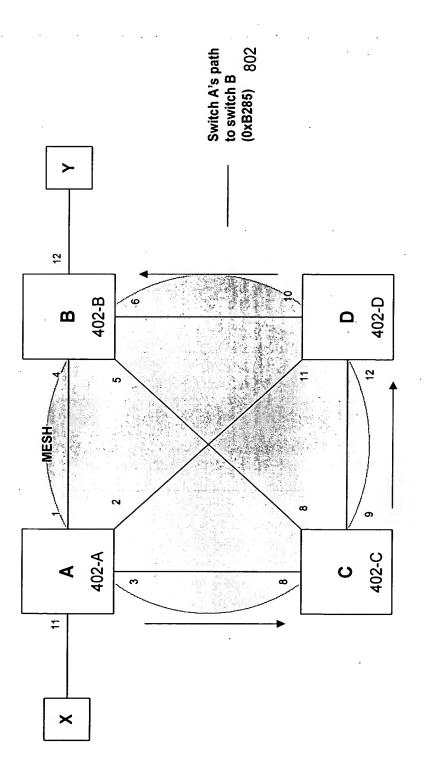


FIG. 8A

